

**In the Specification**

Please amend the paragraph in the "Related Patent Data" section on page 1, as follows:

This patent resulted from a divisional application of U.S. Patent Application Serial No. 10/418,540, filed April 17, 2003, entitled "Method of Forming a Local Interconnect", naming H. Montgomery Manning as inventor, now U.S. Patent No. 6,803,286 B2, the disclosure of which is incorporated by reference; which resulted from a continuation application of U.S. Patent Application Serial No. 10/087,147, filed February 28, 2002, entitled "Methods of Fabricating Integrated Circuitry", naming H. Montgomery Manning as inventor, now U.S. Patent No. 6,638,842 B2, the disclosure of which is incorporated by reference; which resulted from a divisional application of application of U.S. Patent Application Serial No. 09/608,333, filed June 29, 2000, entitled "Method of Forming a Local Interconnect", naming H. Montgomery Manning as inventor, now U.S. Patent No. 6,391,726 B1, the disclosure of which is incorporated by reference; which resulted from a divisional application of U.S. Patent Application Serial No. 09/266,456, filed March 11, 1999, entitled "Integrated Circuitry, Methods of Fabricating Integrated Circuitry, Method of Forming Local Interconnects, and Methods of

Forming Conductive Lines", naming H. Montgomery Manning as inventor, now U.S. Patent No. 6,180,494, the disclosure of which is incorporated by reference.